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Range (SFDR), Integral Non-Linearity (INL), Differential Non-Linearity (DNL) and sampling clock jitter requirements. Relations between these specifications and practical issues like matching of components and offset parameters of differential pairs are derived. CMOS Integrated Analog-to-Digital and Digital-to-Analog Converters describes the requirements of input and signal reconstruction filtering in case a converter is applied into a signal processing system. CMOS Integrated Analog-to-Digital and Digital-to-Analog Converters describes design details of high-speed A/D and D/A converters, high-

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resolution A/D and D/A converters, sample-and-hold amplifiers, voltage and current references, noise-shaping converters and sigma-delta converters, technology parameters and matching performance, comparators and limitations of comparators and finally testing of converters.

CMOS chips are becoming increasingly important in computer circuitry. They have been widely used during the past decade, and they will continue to grow in popularity in

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those application areas that demand high performance. Challenging the prevailing opinion that circuit simulation can reveal all problems in CMOS circuits, Masakazu Shoji maintains that simulation cannot completely remove the often costly errors that occur in circuit design. To address the failure modes of these circuits more fully, he presents a new approach to CMOS circuit design based on his systematizing of circuit design error and his unique theory of CMOS digital circuit operation. In analyzing CMOS digital circuits, the author focuses not on effects originating from the characteristics of the

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device (MOSFET) but on those arising from their connection. This emphasis allows him to formulate a powerful but ultimately simple theory explaining the effects of connectivity by using a concept of the states of the circuits, called microstates. Shoji introduces microstate sequence diagrams that describe the state changes (or the circuit connectivity changes), and he uses his microstate theory to analyze many of the conventional CMOS digital circuits. These analyses are practically all in closed-form, and they provide easy physical interpretation of the circuit's working mechanisms, the

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parametric dependence of performance, and the circuit's failure modes. Originally published in 1992. The Princeton Legacy Library uses the latest print-on-demand technology to again make available previously out-of-print books from the distinguished backlist of Princeton University Press. These editions preserve the original texts of these important books while presenting them in durable paperback and hardcover editions. The goal of the Princeton Legacy Library is to vastly increase access to the rich scholarly heritage found in the thousands of books published by Princeton University Press since

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High-speed, power-efficient analog integrated circuits can be used as standalone devices or to interface modern digital signal processors and micro-controllers in various applications, including multimedia, communication, instrumentation, and control systems. New architectures and low device geometry of complementary metaloxidesemiconductor (CMOS) technologies have accelerated the movement toward system on a chip design, which merges analog circuits with digital, and radio-frequency components. CMOS: Analog Integrated Circuits: High-Speed and Power-Efficient Design

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describes the important trends in designing these analog circuits and provides a complete, in-depth examination of design techniques and circuit architectures, emphasizing practical aspects of integrated circuit implementation. Focusing on designing and verifying analog integrated circuits, the author reviews design techniques for more complex components such as amplifiers, comparators, and multipliers. The book details all aspects, from specification to the final chip, of the development and implementation process of filters, analog-to-digital converters (ADCs), digital-to-analog

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converters (DACs), phase-locked loops (PLLs), and delay-locked loops (DLLs). It also describes different equivalent transistor models, design and fabrication considerations for high-density integrated circuits in deep-submicrometer process, circuit structures for the design of current mirrors and voltage references, topologies of suitable amplifiers, continuous-time and switched-capacitor circuits, modulator architectures, and approaches to improve linearity of Nyquist converters. The text addresses the architectures and performance limitation issues affecting circuit operation and

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provides conceptual and practical solutions to problems that can arise in the design process. This reference provides balanced coverage of theoretical and practical issues that will allow the reader to design CMOS analog integrated circuits with improved electrical performance. The chapters contain easy-to-follow mathematical derivations of all equations and formulas, graphical plots, and open-ended design problems to help determine most suitable architecture for a given set of performance specifications. This comprehensive and illustrative text for the design and analysis of CMOS analog integrated

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circuits serves as a valuable resource for analog circuit designers and graduate students in electrical engineering.

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