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yet to be successfully integrated is the Power Amplifier (PA). The PA is usually the last active building block in a radio transmitter. Its function is to amplify the signal power up to the required level

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before it can be transmitted into the air. Due to several limitations of CMOS technology, designing a linear and efficient PA is a challenging task.

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This thesis addresses the potential of integrating linear and power-efficient PAs in nanometer CMOS technologies at GHz frequencies. In total eight amplifiers have been designed - two linear Class-A PAs, two switched Class-E PAs, and

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four Class-D PAs linearized in
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on the outphasing PAs, amplifier
models

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The RF power amplifier is a key
component in a wireless

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Abstract: This paper presents a highly linear differential cascode CMOS power amplifier (PA) with gate bias circuits in Common

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Source (CS) and Common Gate (CG) amplifiers. The proposed Class-D bias circuit at the gate of a CS amplifier injects a reshaped envelope signal only when the envelope signal is above a certain threshold voltage.

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Toshiba has released an micro-power CMOS operational amplifier, drawing a maximum of 600nA and typically 270nA (1.5V supply, -40 to 105 ° C). Called TC75S102F, it will run from supplies between

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1.5V and 5.5V and is rail-to-rail on both input and output. “ Operational amplifiers have a multitude of ...

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The work establishes the design flow for the optimization of linear CMOS power amplifiers from the first steps of the design to the final IC implementation and tests. The authors also focuses on design guidelines of the inductor ' s geometrical characteristics for

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Wireless Applications and covers their measurement and characterization. Additionally, a model is proposed which would facilitate designs in terms of transistor sizing, required inductor quality factors or minimum supply voltage. The model considers

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limitations that CMOS processes
can impose on implementation.

The book also provides different
techniques and architectures that
allow for optimization.

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Advances in electronics have
pushed mankind to create devices,

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ranging from - credible gadgets to medical equipment to spacecraft instruments. More than that, modern society is getting used to—if not dependent on—the comfort, solutions, and astonishing amount of information brought by these devices. One field that has

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continuously benefited from those advances is the radio frequency integrated circuit (RFIC) design, which in its turn has promoted countless benefits to the industry as a payoff. Wireless communications is one prominent example of what the advances in

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electronics have enabled and their consequences to our daily life. How could anyone back in the eighties think of the possibilities opened by the wireless local area networks (WLANs) that can be found today in a host of places, such as public libraries, coffee

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shops, trains, to name just a few?
How can a youngster, who lives
this true WLAN experience
nowadays, imagine a world without
it? This book deals with the design
of linear CMOS RF
Power Amplifiers (PAs). The RF PA
is a very important part of the RF

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transceiver, the device that
enables wireless communications.

Two important aspects that are
key to keep the advances in RF PA
design at an accelerate pace are
treated: efficiency enhancement
and frequency-tunable capability.

For this purpose, the design of two

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different integrated circuits realized in a 0.11 μm technology is presented, each one addressing a different aspect. With respect to efficiency enhancement, the design of a dynamic supply RF power amplifier is treated, making up the material of Chaps. 2 to 4.

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The work establishes the design flow for the optimization of linear CMOS power amplifiers from the first steps of the design to the final IC implementation and tests. The authors also focuses on design guidelines of the inductor ' s

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geometrical characteristics for power applications and covers their measurement and characterization. Additionally, a model is proposed which would facilitate designs in terms of transistor sizing, required inductor quality factors or minimum supply

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voltage. The model considers limitations that CMOS processes can impose on implementation. The book also provides different techniques and architectures that allow for optimization.

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focuses on the design procedure
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power amplifiers. This is the first
monograph addressing RF CMOS
power amplifier design for

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Wireless Applications. The focus on power amplifiers for short is distance wireless personal and local area networks (PAN and LAN), however the design techniques are also applicable to emerging wide area networks (WAN) infrastructure using micro

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Wireless cell networks. The book discusses CMOS power amplifier design principles and theory and describes the architectures and tradeoffs in designing linear and nonlinear power amplifiers. It then details design examples of RF CMOS power amplifiers for short

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distance wireless applications (e, g., Bluetooth, WLAN) including designs for multi-standard platforms. Design aspects of RF circuits in deep submicron CMOS are also discussed. RF CMOS Power Amplifiers: Theory Design and Implementation serves as a

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reference for RF IC design engineers and RD and R&D managers in industry, and for graduate students conducting research in wireless semiconductor IC design in general and with CMOS technology in particular.

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This book tackles both high efficiency and high linearity power amplifier (PA) design in low-voltage CMOS. With its emphasis on theory, design and implementation, the book offers a guide for those actively involved in

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the design of fully integrated CMOS wireless transceivers. Offering mathematical background, as well as intuitive insight, the book is essential reading for RF design engineers and researchers and is also suitable as a text book.

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Techniques for Frequencies from
Audio to RF deals with the
complicated issues involved in the
design of high-linearity integrated

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wideband amplifiers for different operating frequencies. The book demonstrates these principles using a number of high-performance designs. New topologies for high linearity are presented, as well as a novel method for estimating the

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intermodulation distortion of a wideband signal. One of the most exciting results presented is an enhanced feedback configuration called feedback boosting that is capable of very low distortion. Also important is a statistical method for relating the

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intermodulation distortion of a wideband signal to the total harmonic distortion (THD) of a single tone. The THD, as opposed to the intermodulation distortion of the wideband signal, is easy to measure and use as a design parameter. Three different

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Applications where high linearity is needed are identified, namely audio power amplifiers, wideband IF amplifiers and RF power amplifiers. For these applications high-performance integrated amplifier designs using novel topologies are presented together

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with measurement results. The audio amplifiers are built in CMOS and are capable of driving 80 Ω loudspeaker loads directly without using any external components. One of the designs can operate on a supply voltage down to 1.5V. Both bipolar and CMOS wideband

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IF amplifiers are built; they are fully differential and have linearity from DC to 20 MHz. Finally, an RF power amplifier is built in CMOS, without using inductors, in order to investigate what performance can be achieved without them. Highly Linear Integrated Wideband

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reference for researchers and
designers of integrated amplifiers,
and may be used as a text for
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performance radio frequency
integrated circuits (RF IC) design
in CMOS. 1. Development of radio
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communications has been
advancing rapidly in the past two
decades. Many high performance
systems have been developed,

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Wireless Applications (AMPS, GSM, TDMA, CDMA, W-CDMA, etc.), GPS system (global positioning system) and WLAN (wireless local area network) systems. The rapid growth of VLSI technology in both digital circuits and analog circuits

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provides benefits for wireless communication systems. Twenty years ago not many people could imagine millions of transistors in a single chip or a complete radio for size of a penny. Now not only complete radios have been put in a single chip, but also more and

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more functions have been realized by a single chip and at a much lower price. A radio transmits and receives electro-magnetic signals through the air. The signals are usually transmitted on high frequency carriers. For example, a typical voice signal requires only

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30 Kilohertz bandwidth. When it is transmitted by a FM radio station, it is often carried by a frequency in the range of tens of megahertz to hundreds of megahertz. Usually a radio is categorized by its carrier frequency, such as 900 MHz radio or 5 GHz radio. In general, the

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Higher the carrier frequency, the better the directivity, but the more difficult the radio design.

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