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Electronics Interview Questions and Answers | Most asked Interview Questions for freshers | STLD Viva Questions External Practicals | Part-1 | Interview Question | Difference between if-else, if-elseif-else and case statements in verilog/VHDL 8085 | Viva Questions | Architecture | Bharat Acharya Education 8085 8086 MICROPROCESSOR VIVA QUESTIONS AND ANSWERS Electronic Engineering Job Interview Questions (Part 1) Vlsi Lab Viva Questions Answers
300+ TOP VLSI LAB VIVA Questions and Answers 1. Why does the present VLSI circuits use MOSFETs instead of BJTs? Compared to BJTs, MOSFETs can be made very small as... 2. What are the various regions of operation of MOSFET? How are those regions used? MOSFET has three regions of... 3. What is ...

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Answer : Local skew : The difference between the clock reaching at the launching flop vs the clock reaching the destination flip-flop of a timing-path. Global skew : The difference between the earliest reaching flip-flop and latest reaching flip-flop for a same clock-domain.

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You are here: Home 1 / Latest Articles 2 / Heavy Industries 3 / Top 17 VLSI Interview Questions & Answers. Top 17 VLSI Interview Questions & Answers last updated December 14, 2020 / 9 Comments / in Heavy Industries / by admin. 1) Explain how logical gates are controlled by Boolean logic?

Top 17 VLSI Interview Questions & Answers

Vlsi Lab External Viva Question And Answer Author: dc-75c7d428c907.tecadmin.net-2020-11-13T00:00:00+00:01 Subject: Vlsi Lab External Viva Question And Answer Keywords: vlsi, lab, external, viva, question, and, answer Created Date: 11/13/2020 1:15:37 PM

Vlsi Lab External Viva Question And Answer

VLSI Lab viva question with answers 0100, 0010, and 0001. If the value is 0100, then it means second state is the current state. One-Cold encoding: Same as one-hot encoding except that 0 is the valid value. If there are four states then it requires four bits (four flip-flops) to represent the current state.

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VLSI viva questions and answers: 1. Why does the present VLSI circuits use MOSFETs instead of BJTs? Compared to BJTs, MOSFETs can be made very small as they occupy very small silicon area on IC chip and are relatively simple in terms of manufacturing.

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Vlsi Lab Viva Questions With Answers

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8085 Microprocessor Lab Viva Questions With Answers. 1. ... It is a single chip on which a micro processor, memory and I/O signal lines are fabricated using VLSI technology. 27. What is the priority order of hard ware interrupts in 8085 ? Trap, RST 7.5, RST 6.5, RST 5.5 and INTR. Trap has the highest priority and INTR has the lowest.

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Designed as a text for the students of various engineering streams such as electronics/electrical engineering, electronics and communication engineering, computer science and engineering, IT, instrumentation and control and mechanical engineering, this well-written text provides an introduction to electronic devices and circuits. It introduces to the readers electronic circuit analysis and design techniques with emphasis on the operation and use of semiconductor devices. It covers principles of operation, the characteristics and applications of fundamental electronic devices such as p-n junction diodes, bipolar junction transistors (BJTs), and field effect transistors (FETs), and special purpose diodes and transistors. In its second edition, the book includes a new chapter on "special purpose devices". What distinguishes this text is that it explains the concepts and applications of the subject in such a way that even an average student will be able to understand working of electronic devices, analyze, design and simulate electronic circuits. This comprehensive book provides: A large number of solved examples. Summary highlighting the important points in the chapter. A number of Review Questions at the end of each chapter. A fairly large number of unsolved problems with answers.

How should I prepare for a Digital VLSI Verification Interview? What all topics do I need to know before I turn up for an interview? What all concepts do I need to brush up? What all resources do I have at my disposal for preparation? What does an Interviewer expect in an Interview? These are few questions almost all individuals ponder upon before an interview. If you have these questions in your mind, your search ends here as keeping these questions in their minds, authors have written this book that will act as a golden reference for candidates preparing for Digital VLSI Verification Interviews. Aim of this book is to enable the readers practice and grasp important concepts that are applicable to Digital VLSI Verification domain (and Interviews) through Question and Answer approach. To achieve this aim, authors have not restricted themselves just to the answer. While answering the questions in this book, authors have taken utmost care to explain underlying fundamentals and concepts. This book consists of 500+ questions covering wide range of topics that test fundamental concepts through problem statements (a common interview practice which the authors have seen over last several years). These questions and problem statements are spread across nine chapters and each chapter consists of questions to help readers brush-up, test, and hone fundamental concepts that form basis of Digital VLSI Verification. The scope of this book however, goes beyond technical concepts. Behavioral skills also form a critical part of working culture of any company. Hence, this book consists of a section that lists down behavioral interview questions as

well. Topics covered in this book:1. Digital Logic Design (Number Systems, Gates, Combinational, Sequential Circuits, State Machines, and other Design problems)2. Computer Architecture (Processor Architecture, Caches, Memory Systems)3. Programming (Basics, OOP, UNIX/Linux, C/C++, Perl)4. Hardware Description Languages (Verilog, SystemVerilog)5. Fundamentals of Verification (Verification Basics, Strategies, and Thinking problems)6. Verification Methodologies (UVM, Formal, Power, Clocking, Coverage, Assertions)7. Version Control Systems (CVS, GIT, SVN)8. Logical Reasoning/Puzzles (Related to Digital Logic, General Reasoning, Lateral Thinking)9. Non Technical and Behavioral Questions (Most commonly asked)In addition to technical and behavioral part, this book touches upon a typical interview process and gives a glimpse of latest interview trends. It also lists some general tips and Best-Known-Methods to enable the readers follow correct preparation approach from day-1 of their preparations. Knowing what an Interviewer looks for in an interviewee is always an icing on the cake as it helps a person prepare accordingly. Hence, authors of this book spoke to few leaders in the semiconductor industry and asked their personal views on "What do they look for while Interviewing candidates and how do they usually arrive at a decision if a candidate should be hired?". These leaders have been working in the industry from many-many years now and they have interviewed lots of candidates over past several years. Hear directly from these leaders as to what they look for in candidates before hiring them. Enjoy reading this book. Authors are open to your feedback. Please do provide your valuable comments, ratings, and reviews.

The Verilog Hardware Description Language was first introduced in 1984. Over the 20 year history of Verilog, every Verilog engineer has developed his own personal "bag of tricks" for coding with Verilog. These tricks enable modeling or verifying designs more easily and more accurately. Developing this bag of tricks is often based on years of trial and error. Through experience, engineers learn that one specific coding style works best in some circumstances, while in another situation, a different coding style is best. As with any high-level language, Verilog often provides engineers several ways to accomplish a specific task. Wouldn't it be wonderful if an engineer first learning Verilog could start with another engineer's bag of tricks, without having to go through years of trial and error to decide which style is best for which circumstance? That is where this book becomes an invaluable resource. The book presents dozens of Verilog tricks of the trade on how to best use the Verilog HDL for modeling designs at various level of abstraction, and for writing test benches to verify designs. The book not only shows the correct ways of using Verilog for different situations, it also presents alternate styles, and discusses the pros and cons of these styles.

A catalog of solutions to commonly occurring design problems, presenting 23 patterns that allow designers to create flexible and reusable designs for object-oriented software. Describes the circumstances in which each pattern is applicable, and discusses the consequences and trade-offs of using the pattern within a larger design. Patterns are compiled from real systems, and include code for implementation in object-oriented programming languages like C++ and Smalltalk. Includes a bibliography. Annotation copyright by Book News, Inc., Portland, OR

Artificial intelligence (AI) is a field within computer science that is attempting to build enhanced intelligence into computer systems. This book traces the history of the subject, from the early dreams of eighteenth-century (and earlier) pioneers to the more successful work of today's AI engineers. AI is becoming more and more a part of everyone's life. The technology is already embedded in face-recognizing cameras, speech-recognition software, Internet search engines, and health-care robots, among other applications. The book's many diagrams and easy-to-understand descriptions of AI programs will help the casual reader gain an understanding of how these and other AI systems actually work. Its thorough (but unobtrusive) end-of-chapter notes containing citations to important source materials will be of great use to AI scholars and researchers. This book promises to be the definitive history of a field that has captivated the imaginations of scientists, philosophers, and writers for centuries.

In cooperation with experts and practitioners throughout the SOA community, best-selling author Thomas Erl brings together the de facto catalog of design patterns for SOA and service-orientation. More than three years in development and subjected to numerous industry reviews, the 85 patterns in this full-color book provide the most successful and proven design techniques to overcoming the most common and critical problems to achieving modern-day SOA. Through numerous examples, individually documented pattern profiles, and over 400 color illustrations, this book provides in-depth coverage of: " Patterns for the design, implementation, and governance of service inventories"collections of services representing individual service portfolios that can be independently modeled, designed, and evolved. " Patterns specific to service-level architecture which pertain to a wide range of design areas, including contract design, security, legacy encapsulation, reliability, scalability, and a variety of implementation and governance issues. " Service composition patterns that address the many aspects associated with combining services into aggregate distributed solutions, including topics such as runtime messaging and message design, inter-service security controls, and transformation. " Compound patterns (such as Enterprise Service Bus and Orchestration) and recommended pattern application sequences that establish foundational processes. The book begins by establishing SOA types that are referenced throughout the patterns and then form the basis of a final chapter that discusses the architectural impact of service-oriented computing in general. These chapters bookend the pattern catalog to provide a clear link between SOA design patterns, the strategic goals of service-oriented computing, different SOA types, and the service-orientation design paradigm. This book series is further supported by a series of resources sites, including soabooks.com, soaspecs.com, soapatterns.org, soamag.com, and soaposters.com.

This book introduces the reader to FPGA based design for RTL synthesis. It describes simple to complex RTL design scenarios using SystemVerilog. The book builds the story from basic fundamentals of FPGA based designs to advance RTL design and verification concepts using SystemVerilog. It provides practical information on the issues in the RTL design and verification and how to overcome these. It focuses on writing efficient RTL codes using SystemVerilog, covers design for the Xilinx FPGAs and also includes implementable code examples. The contents of this book cover improvement of design performance, assertion based verification, verification planning, and architecture and system testing using FPGAs. The book can be used for classroom teaching or as a supplement in lab work for undergraduate and graduate coursework as well as for professional development and training programs. It will also be of interest to researchers and professionals interested in the RTL design for FPGA and ASIC.

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